

Figure 1

Hybrid Dynamic  
Optimization Process

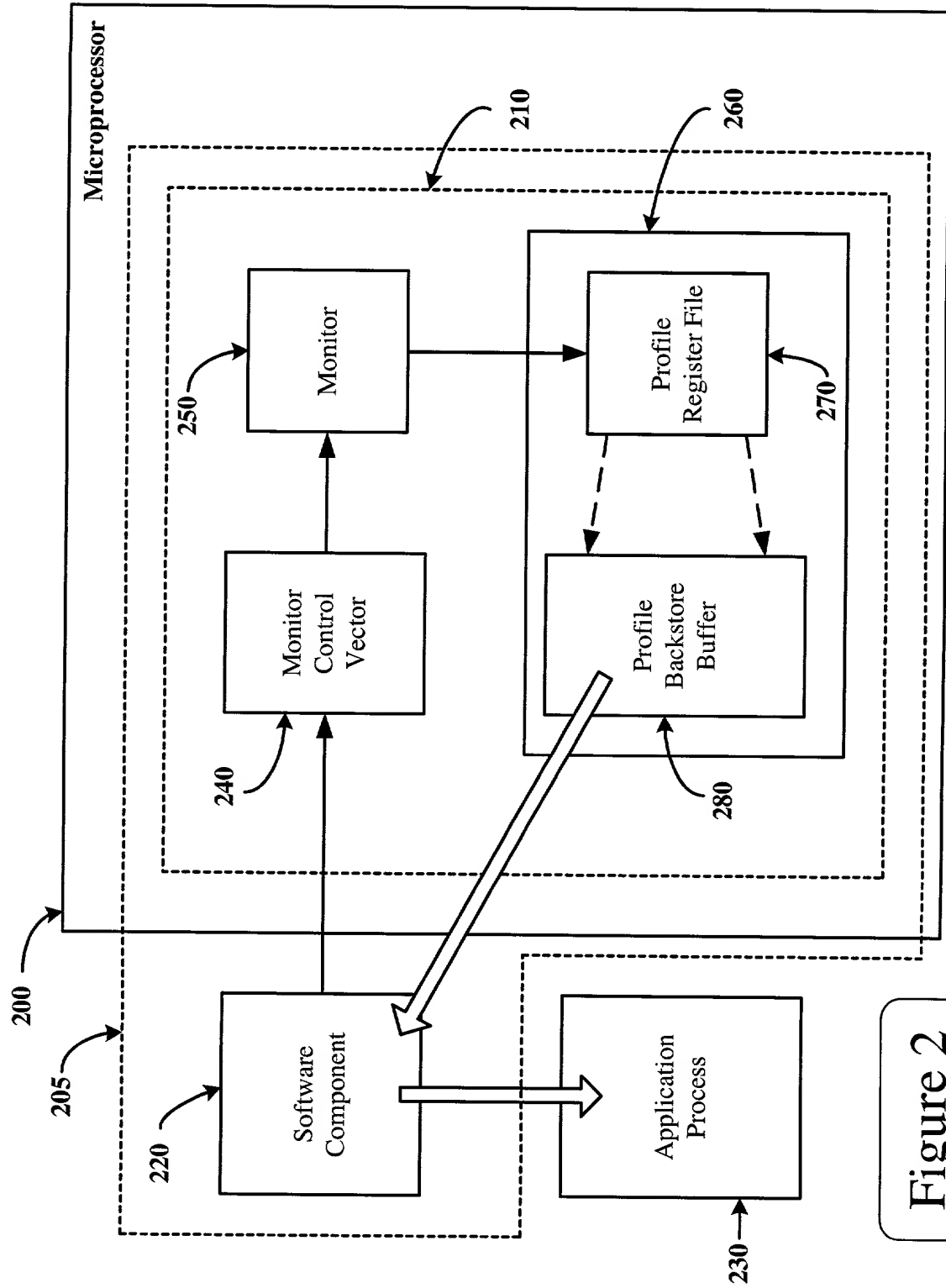


Figure 2

Hybrid Dynamic Optimizer

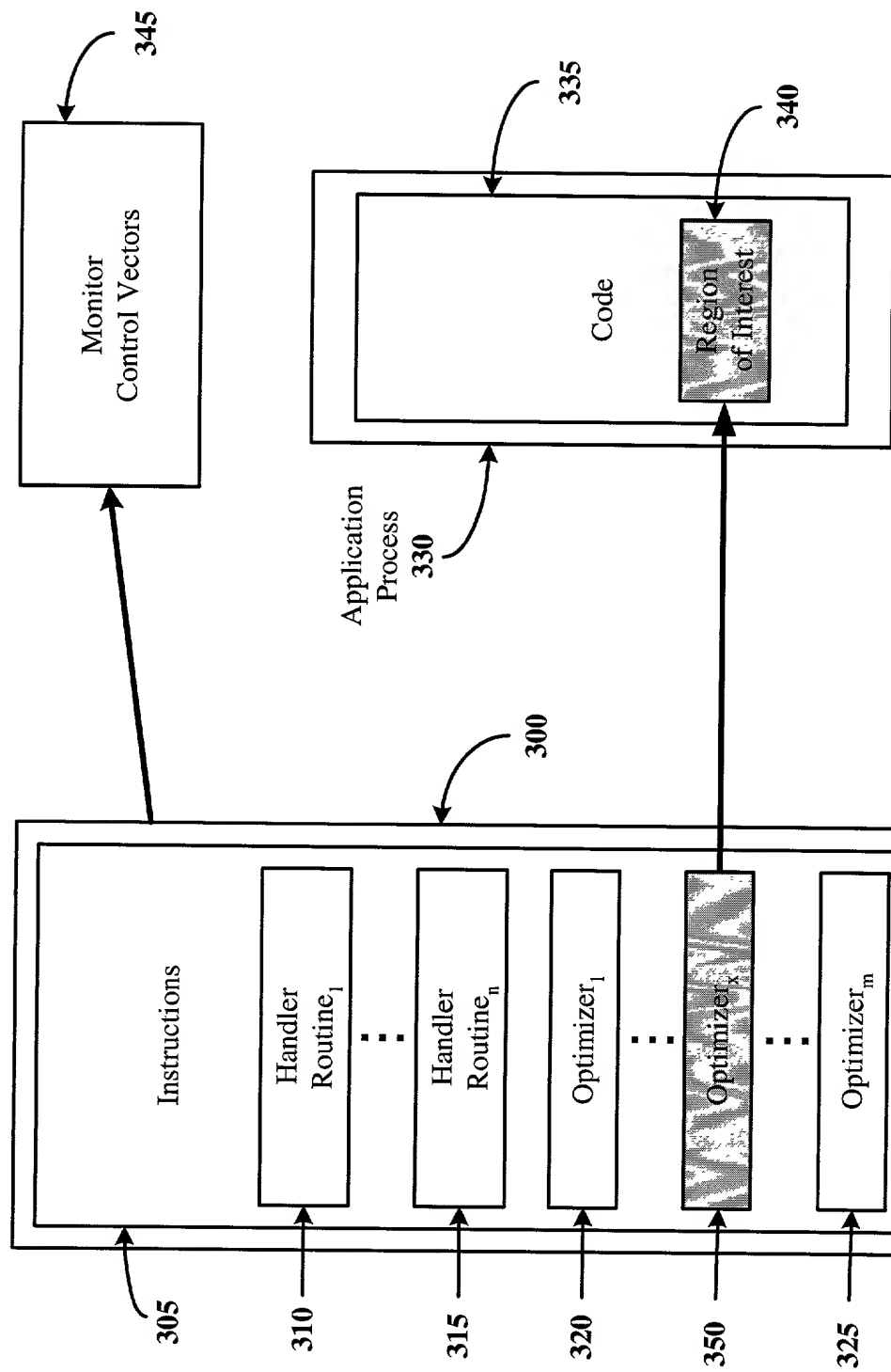


Figure 3

Software  
Component

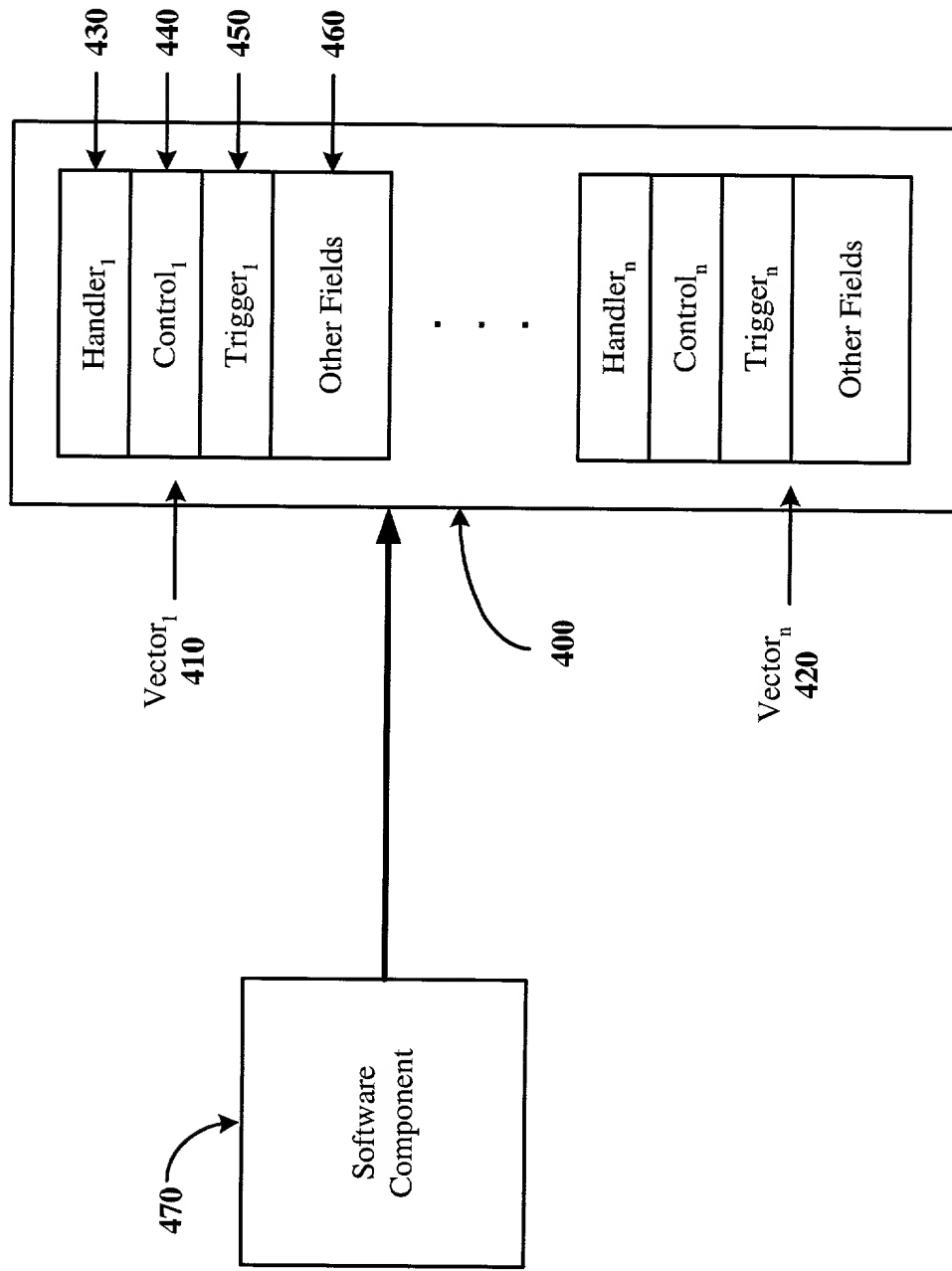


Figure 4

Monitor Control Vectors

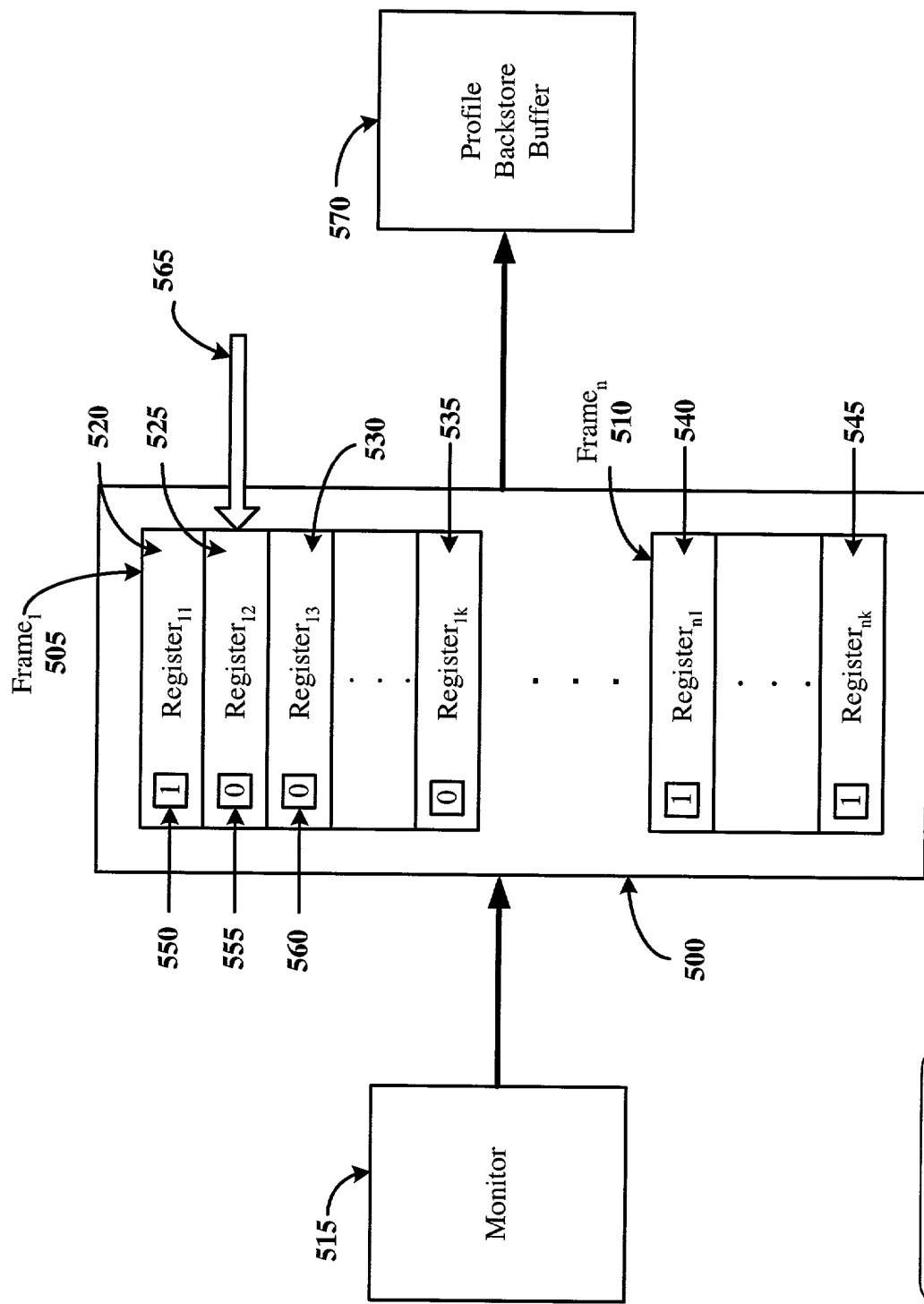


Figure 5

Profile Register File

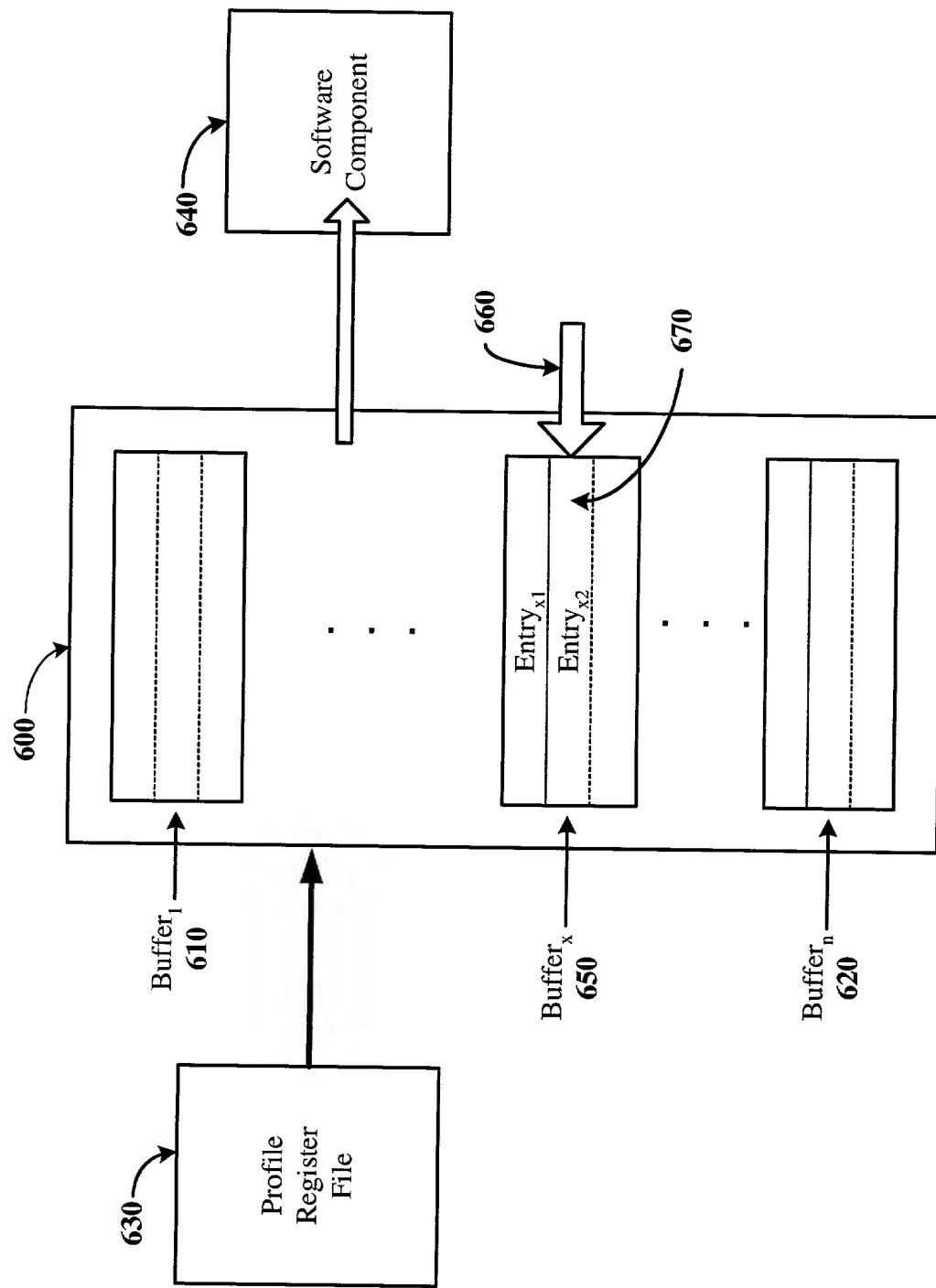


Figure 6  
Profile Backstore Buffer

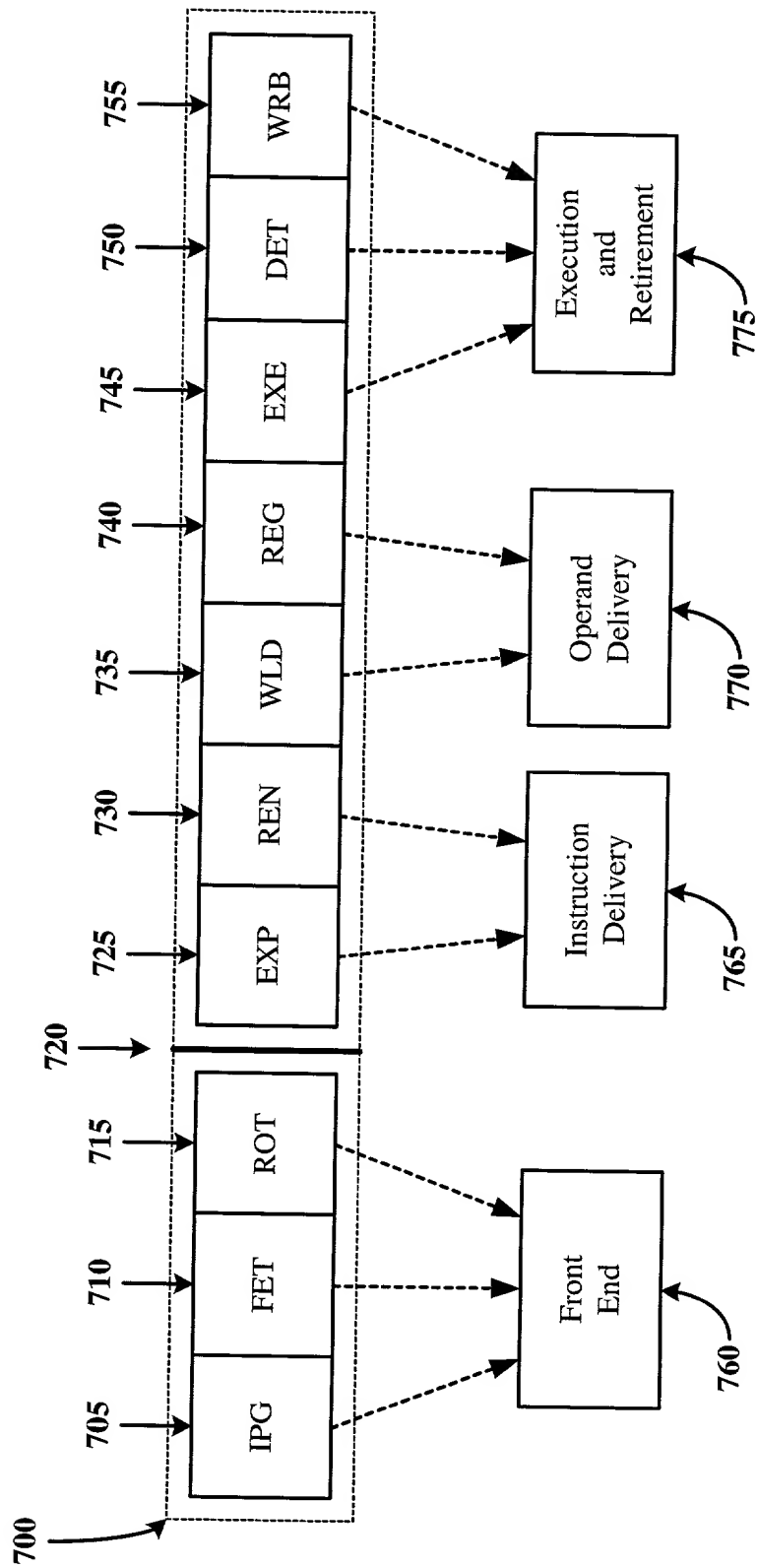


Figure 7

Microprocessor Pipeline